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Title: DAC for high frequency inverter

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Analog Devices" high-speed current steering digital-to-analog converters offer unparalleled AC and DC performance. These products offer the ideal combination of wide spurious-free ...

In this paper, we demonstrate the feasibility of obtaining a voltage-independent capacitor using the standard cells to construct an 8-bit CDAC-based SAR ADC, which does ...

Abstract-- This paper introduces a new dc-dc converter suitable for operation at very high frequencies under on-off control. The converter power stage is based on a resonant inverter ...

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The proposed DAC has been simulated achieving 58 dB SNR and 40 dB SFDR with a 20 uA input current. The circuit demonstrates a DNL of 0.4 LSB and an INL of 0.8 LSB, ...

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While both types of converters are difficult to realize under the given constraints, the challenge is more pronounced for the DACs. Thus, the effort of this project was focused on an aggressive ...

The goal of this document is to introduce a wide range of theories and topics that are relevant to high-speed, digital-to-analog converters (DAC). This document provides details on sampling ...

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