

This PDF is generated from: <https://ruedasenmadrid.es/Fri-04-Sep-2020-13462.html>

Title: High frequency inverter voltage doubler rectification

Generated on: 2026-08-16 06:08:25

Copyright (C) 2026 MADRID MICROGRID. All rights reserved.

For the latest updates and more information, visit our website: <https://ruedasenmadrid.es>

-----

This article proposes an interleaved DC-DC boost architecture with a voltage multiplier rectifier circuit to achieve superior performance.

Resonant Converter with Voltage-Doubler Rectifier or Full-Bridge Rectifier for Wide-Output Voltage and High-Power Applications

The primary inverter bridge can be reconfigured as a full bridge or a half bridge with topology morphing control. Meanwhile, there are three rectification modes: full bridge (FB) ...

To overcome these challenges, a novel higher voltage step-down ICPT topology is proposed by incorporating the hybrid switched capacitor (HSC) inverter and synchronous ...

To address these challenges, this paper proposes a novel rectification circuit based on the VDR topology, specifically designed for LLC resonant converters, offering ...

This paper presents a highly efficient class-F rectifier based on a harmonic-controlled voltage doubler (HCVD) structure. Previous studies on voltage doubler-based class ...

This paper proposes a novel rectification circuit based on the voltage-doubler rectifier (VDR) topology for LLC resonant converters to ...

In order to obtain output voltage with better performance, the effects of the working frequency, the stage capacitance and the load resistance on the output voltage of the voltage ...

To overcome these challenges, a novel higher voltage step-down ICPT topology is proposed by incorporating

the hybrid switched ...

This paper proposes a novel rectification circuit based on the voltage-doubler rectifier (VDR) topology for LLC resonant converters to address these challenges effectively.

This document aims at displaying the proposal for controller optimization for a voltage-doubler boost rectifier with a high power factor. In this order is employed heuristic ...

This document aims at displaying the proposal for controller optimization for a voltage-doubler boost rectifier with a high power factor. ...

Web: <https://ruedasenmadrid.es>

